

Adjustable shunt voltage reference IC

General Description:

The uP431 series ICs are three-terminal adjustable shunt regulators with guaranteed thermal stability over a full operation range. These ICs feature very sharp turn-on characteristics, low temperature coefficient and low output impedance, which make them ideal substitutes for Zener diodes in applications such as switching power supply, charger, and other adjustable regulators.

The uP431 is especially suitable for industry applications types.

The uP431 precision reference is offered in two band-gap tolerance: A: 0.4% and B:1%.

The uP431 are characterized for operation from -40°C to 125°C.

Features:

- Reference Voltage Tolerance at 25°C
 - 0.4% (A Grade)
 - 1% (B Grade)
- Adjustable Output Voltage: V_{REF} to 46 V
- Operation From -40°C to 125°C
- Typical Temperature Drift
 - 6mV (-40 to 85°C)
 - 14 mV (-40 to 125°C)
- Low Output Noise
- Sink-Current Capability: 1 mA to 100 mA

Applications:

- Precision Voltage Reference
- Switching Power Supply
- Charger
- Voltage Adapter
- Adjusted Power Supply

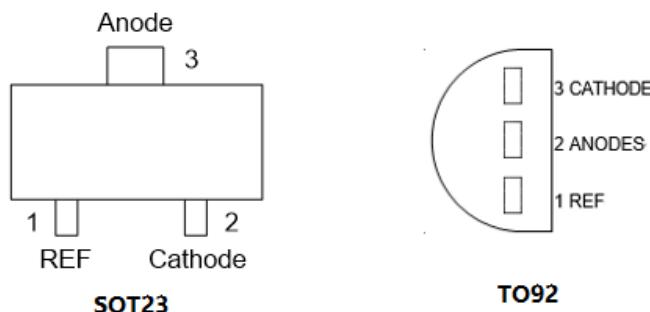


Figure 1. Package types of uP431

Ordering Information

Device Name	Package Types	Accuracy	Mark ID	Operating Free-Air Temperature (TA)
uP431AN	SOT23	0.4%	GB3	-40°C to 125°C
uP431BN	SOT23	1%	GB6	
uP431AZ	TO-92	0.4%	uP431AZ	
uP431BZ	TO-92	1%	uP431BZ	

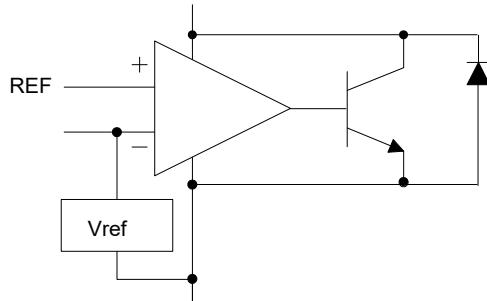
Functional Block Diagram:

Figure 2. Functional Block Diagram of uP431

Absolute Maximum Ratings:

(Operation temperature range applies unless otherwise specified.)

Parameter	Symbol	Range	Unit
Cathode voltage	V_{KA}	46	V
Continuous cathode current	I_{KA}	-100 to 150	mA
Reference input current range	I_{REF}	-0.05 to 10	mA
Maximum junction temperature	T_{JMAX}	150	°C
Maximum lead temperature	T_{LEAD}	260	°C
Storage temperature	T_{STG}	-65 to 150	°C
Thermal resistance SOT23	θ_{JC}	76	°C/W
ESD rating	CDM	+/-1000	V

Note: 1. Absolute maximum ratings indicate stresses beyond which may cause permanent damage to the device.

Recommended Operation Ratings:

Parameter	Symbol	Min.	Max.	Unit
Cathode Voltage	V_{KA}	V_{REF}	46	V
Cathode Current	I_{KA}	1.0	100	mA
Operating Ambient Temperature Range		-40	125	°C

Adjustable shunt voltage reference IC

Electrical Characteristics:

(Typical and limits appearing in normal tape apply for $T_J=25^\circ\text{C}$ unless otherwise noted.)

Parameter		Test Circuit	Symbol	Conditions	UP431			Unit
					Min.	Typ.	Max.	
Reference Voltage	A: 0.4%	Fig.3	V_{REF}	$V_{\text{KA}}=V_{\text{REF}}$ $I_{\text{KA}}=10\text{mA}$	2.490	2.500	2.510	V
	B: 1%				2.475	2.500	2.525	
Deviation of Reference Voltage Over-Temperature		Fig.3	ΔV_{REF}	$V_{\text{KA}}=V_{\text{REF}}$ $I_{\text{KA}}=10\text{mA}$	-40 to 85°C	-	6	12
					-40 to 125°C	-	6	20
Ratio of Change in Reference Voltage to the Change in Cathode Voltage		Fig.4	$\frac{\Delta V_{\text{REF}}}{\Delta V_{\text{KA}}}$	$I_{\text{KA}}=10\text{mA}$	$\Delta V_{\text{KA}}=10\text{V to } V_{\text{REF}}$	-	-1.0	-2.7
					$\Delta V_{\text{KA}}=46\text{V o } 10\text{V}$	-	-0.5	-2.0
Reference Current		Fig.4	I_{REF}	$I_{\text{KA}}=10\text{mA},$ $R_1=10\text{k}\Omega, R_2=\infty$		-	0.7	4
Deviation of Reference Current Over Full Temperature Range		Fig.4	ΔI_{REF}	$I_{\text{KA}}=10\text{mA},$ $R_1=0\text{K}\Omega, R_2=\infty$ $T_A=-40 \text{ to } 105^\circ\text{C}$		-	0.4	1.2
Minimum Cathode Current for Regulation		Fig.4	$I_{\text{KA}}(\text{MIN})$	$V_{\text{KA}}=V_{\text{REF}}$		-	0.4	0.6
Off-State Cathode Current		Fig.5	$I_{\text{KA}}(\text{OFF})$	$V_{\text{KA}}=46\text{ V},$ $V_{\text{REF}}=0$		-	0.1	0.5
Dynamic Impedance		Fig.3	$ Z_{\text{KA}} $	$V_{\text{KA}}=V_{\text{REF}},$ $I_{\text{KA}}=1 \text{ to } 100\text{mA}$ $f \leq 1.0\text{kHz}$		-	0.2	0.5

Parameter Measurement Information

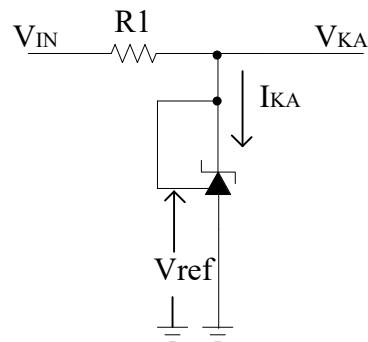


Figure 3. Test Circuit for $V_{KA}=V_{ref}$

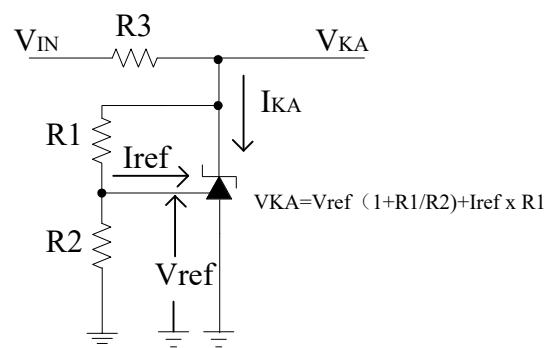


Figure 4. Test Circuit for $V_{KA}>V_{ref}$

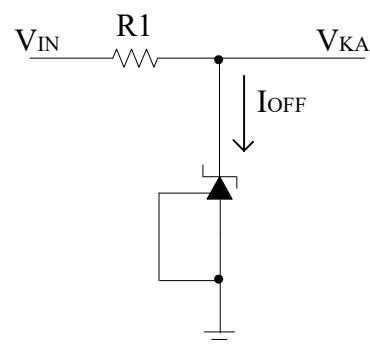
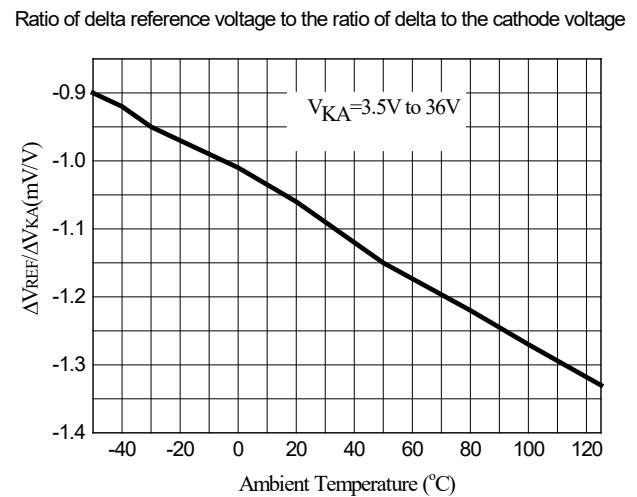
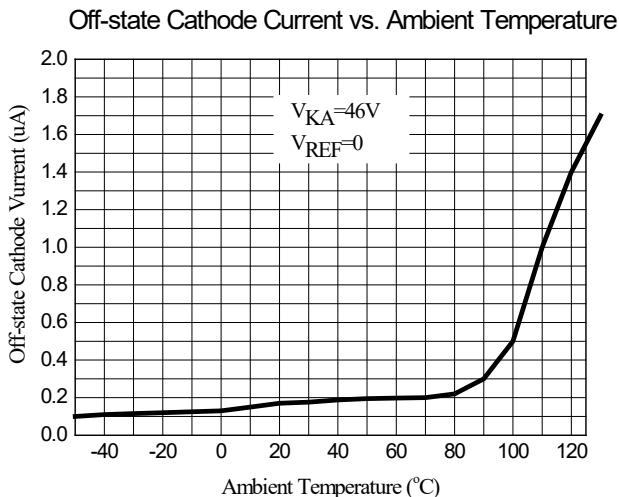
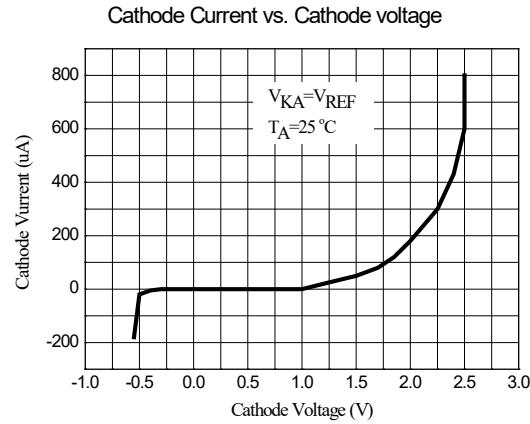
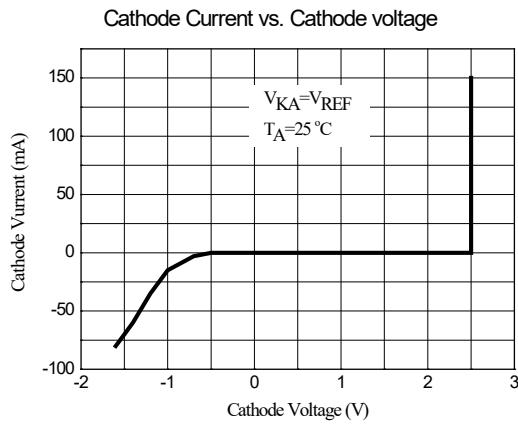
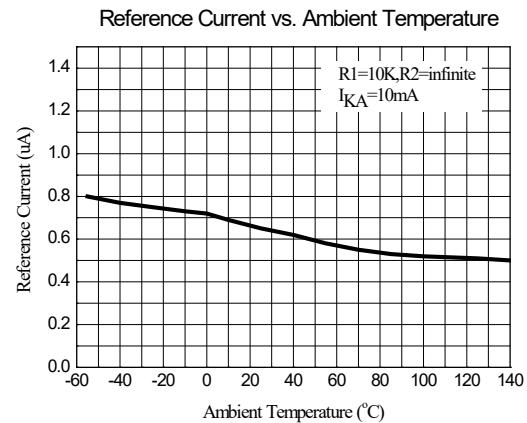
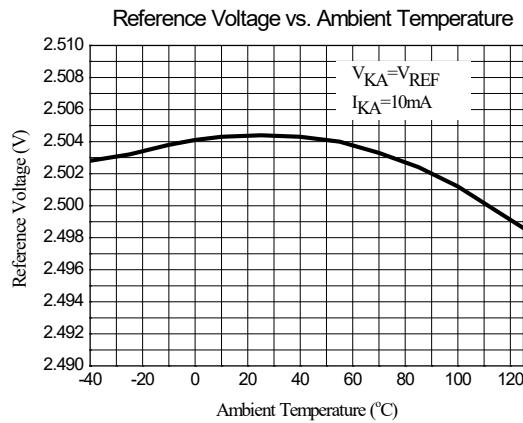
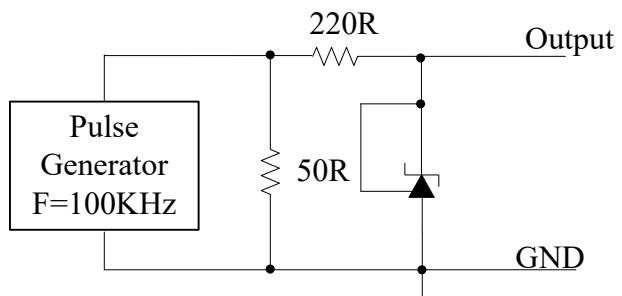
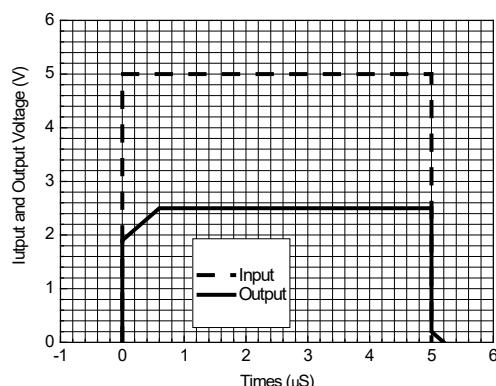
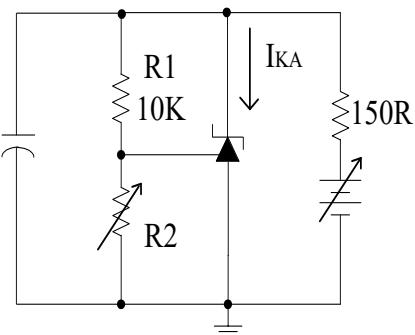
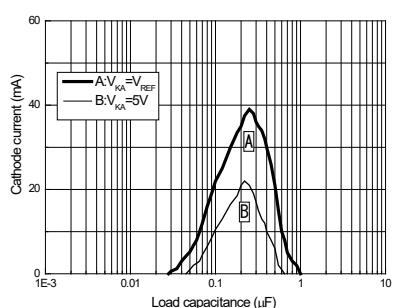
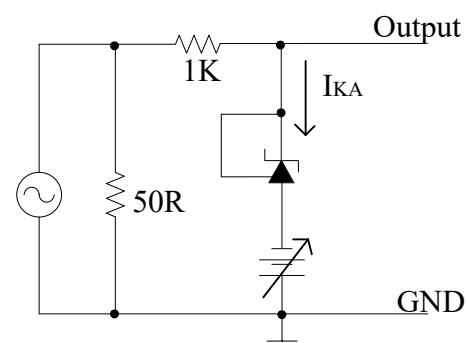
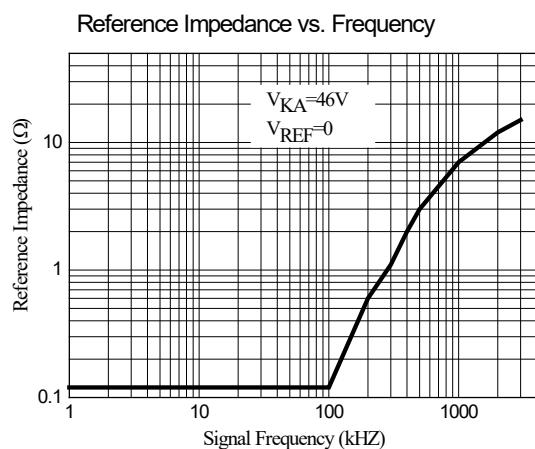
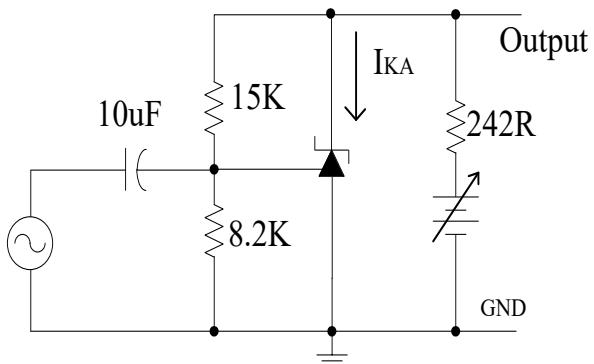
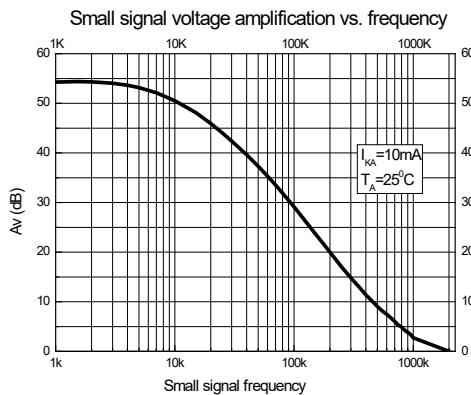


Figure 5. Test Circuit for I_{OFF}

Performance Characteristics



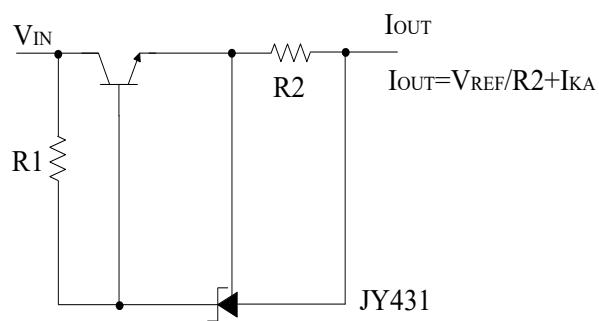
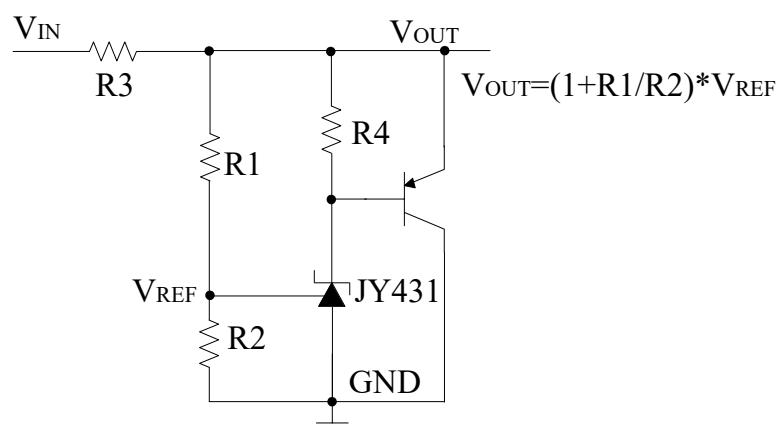
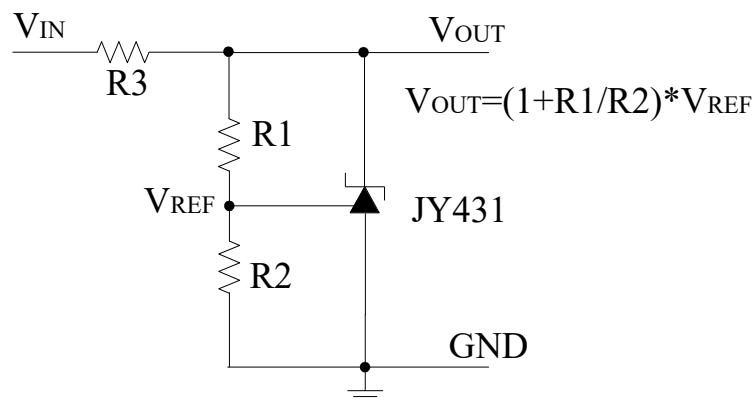
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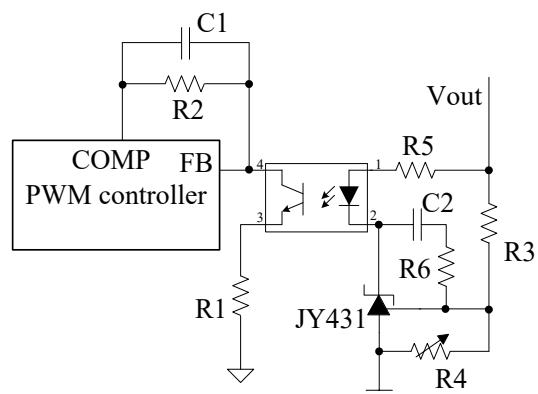
uP431

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Typical Applications Circuit



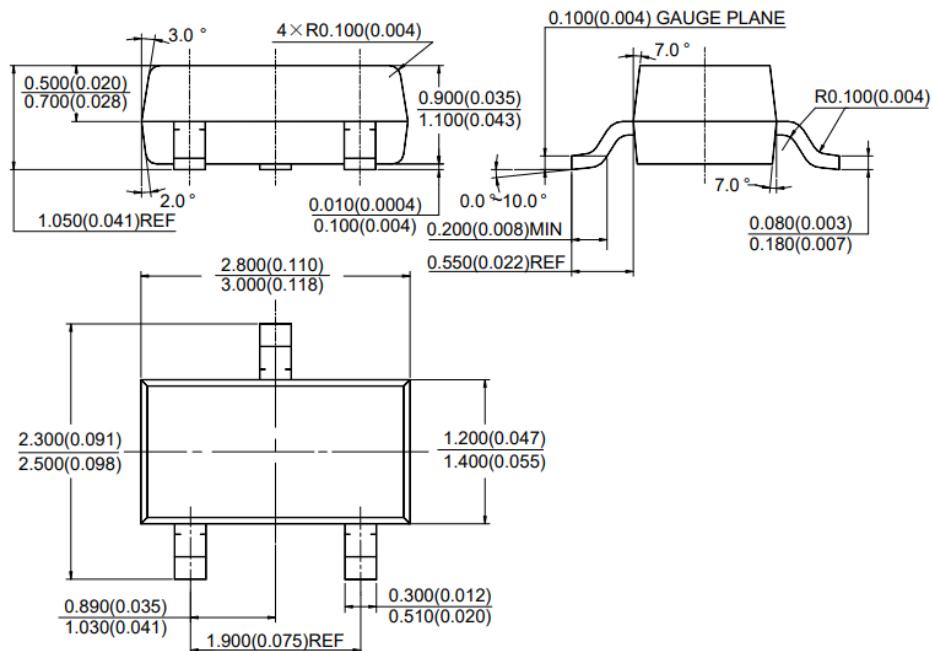
Current Source or Current Limit



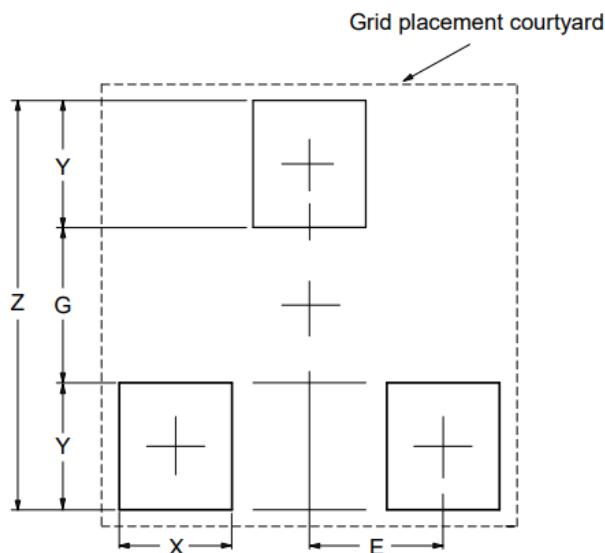
PWM Converter with Reference

Package Outline Dimensions:

SOT-23

**Suggested Pad Layout:**

Package Type: SOT23



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	2.900/0.114	1.100/0.043	0.800/0.031	0.900/0.035	0.950/0.037