1. Features

- Wide input AC range: 90Vac~264Vac
- Output power: 3W~5W
- Built-in power transistor
- Up to 80KHz operating frequency
- Adjustable cable compensation (3%~8%)
- Quasi-resonant turn on
- DoE(VI)/CoC tier2 compliant efficiency
- Less than 70mW standby power @230Vac
- Over temperature protection

- VCC over-voltage protection
- Output over-voltage protection
- Output short circuit protection
- Single fault protection

2. Applications

- Mobile phone chargers
- Cordless phone adapters
- LED driver
- Standby power supplies

3. Typical applications (5V/1A USB charger)

Parameter	Symbol	Value	Unit	Condition
AC supply	Vac	90~264	V	
Output voltage	Vo	5	V	At cable end
Output voltage ripple	VRIPPLE	<150	mV	At cable end
Output current	Iomax	1.08	A	S
Switching frequency	fmax	50	KHz	At 5V/1.08A
Cable compensation	Vcab/Vo	3%		
No input load power	P _{NL}	<70	mW	Per energy star test method
Average efficiency	η	79%	Y	At cable end
AC on time delay	Ton	<3	S	At 90Vac

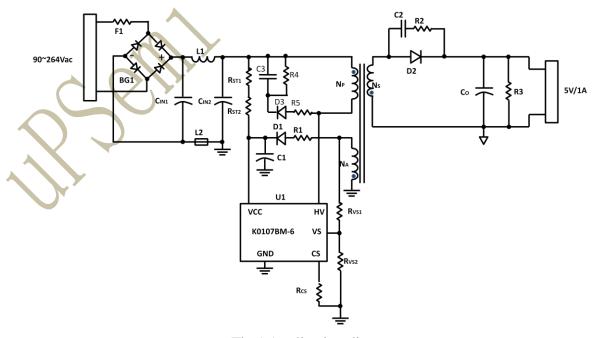


Fig.1 Application diagram

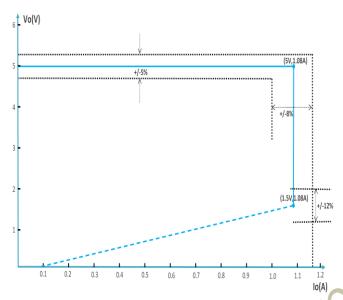
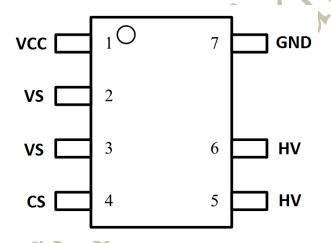


Fig.2 I-V curve of 5W USB charger

4. Pin definitions



K0107BM-6

Pin Name	Pin Type	Pinout	Pin Functions
VCC	Power	1	Supply of operating current of the controller
VS	Input	2	Voltage sense of secondary winding
VS	Input	3	Voltage sense of secondary winding
CS	Input	4	Current sense of BJT emitter current
GND	Ground	7	Ground
HV	I/O	5,6	Collector of the power BJT

5. Absolute maximum ratings (Note 1)

Parameter	Name	Range	Unit
Voltage range	HV	-0.5 to 800	V
Voltage range	VCC	-0.5 to 30	V
Voltage range	VS	-30 to 7	V
Voltage range	CS	-0.5 to 7	V
Voltage range(K)	OUT	-0.5 to 7	V
Maximum junction temperature	Тлмах	150	\mathcal{C}
Lead temperature	TLEAD	260	C
Storage temperature	Tstg	-55 to 150	\mathcal{C}
ESD rating per ANSI/STM5.1-2001	HBM	2000	V
ESD rating per JEDEC JESD22-C101F	CDM	1000	V
Latchup test per JEDEC 78D		+/-200	mA

Note1: Stresses over those listed under "Absolute maximum ratings" may cause permanent damages to the device. These are stress ratings only. Functional operation beyond those under "Recommended operating conditions" is not implied.

6. Thermal parameter

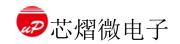
Junction to ambient thermal resistance			Ө ЈА	140	℃/W
Over temperature protection			Тотр*	160	$\mathcal C$

^{*}Typical, guarantee by design

7. Electrical parameter

Ta=25 ℃, unless otherwise specified

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Power supply(VCC pin)						
VCC overvoltage protection	VCCOVP		35	37	39	V
Quiescent current	Icc	VCC=12V	260	350	440	μΑ
Startup voltage	Vst		12.75	15	17.25	V
Minimum operating voltage	Vuvlo		3.5	4.2	4.9	V
Startup current	Isт	VCC=Vst-0.5V		0.4	0.6	μΑ
Constant voltage control (VS p	in)					
VS regulation voltage	V_{FB}		4.05	4.1	4.15	V
Cable compensation current	Icab	At no load		50		μΑ
Sampling instant/Tons	Tsam/Tons	@Vcsmax		66		%



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Datasl	neet	1.	2

Min. operating frequency	fmin		71	83	95	Hz		
Constant current control (CS pin)								
Max. shutdown voltage	Vcsmax		500	525	550	mV		
Min. shutdown voltage	VCSMIN			300		mV		
Leading edge blanking	TLEB			360		nS		
Maximum duty cycle	DSMAX			0.5				
Protection functions								
Over temperature	Тотр		130	160	190	${\mathcal L}$		
Output over voltage	VFBOVP			5.3		V		
Output short circuit voltage	VFBHICCUP			1		V		

8. Functional block diagram

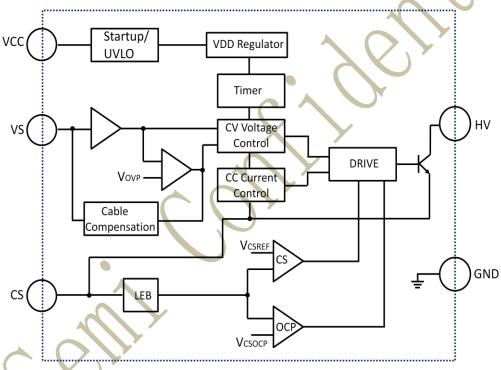


Fig.3, K0107BM-6 block diagram

9. Principle of operation

The K0107BM-6 is a high performance offline AC-DC switcher for charger and adapter applications up to 5W. The devices operate in Discontinuous Conduction Mode (DCM) with Primary Side Regulation (PSR) to achieve Constant Voltage (CV) and Constant Current (CC) in the whole load range.

9.1 Power up and power down sequences

Refer to Fig.2 and Fig.3, after AC power supply is applied to the converter, VCC capacitor C1 is charged via the startup resistors RsT1 and RsT2. When VCC voltage reaches startup voltage VsT, the switcher U1 starts to work. The built-in power transistor is turned on, and voltage on CS pin is

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ramping up as the current through the primary winding generates voltage drop across the current sense resistor Rcs. When the CS pin voltage reaches Vcsref after the Leading Edge Blanking (LEB) time Tleb, the controller turns off the power transistor, then generates next turn on event according to the load conditions of the charger/adapter. When the AC power is removed, there is no sufficient energy in the input capacitor Ciniand Cini, the VCC voltage continues dropping. When VCC voltage drops below Vuvlo, the built-in power transistor is forbidden to be turn on, the controller waits for the VCC voltage to be higher than Vst for a new round startup.

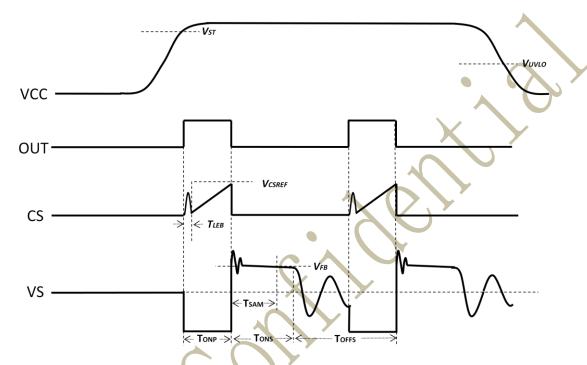


Fig.4, switching waveforms of typical application

9.2 Constant Voltage (CV) operation

Constant voltage operation occurs when the load is between no-load and full-load. Output voltage is sensed at the VS pin, which is connected to the auxiliary winding via resistors Rvs1 and Rvs2. As shown in Fig.5, the VS waveform is sampled at Tsam, around 2/3 duration of the secondary winding conduction time(Tons). The sampled voltage is regulated at Vfb by the voltage control loop. The CV output is determined by the resistors Rvs1, Rvs2 and the turn ratio of secondary winding to auxiliary winding (Ns/Na). Due to system ESD considerations, the sum of Rvs1 and Rvs2 is suggested in the range of 25K Ω to 75 K Ω . The VS pin sources a current which is inverse proportional to load current to generate cable compensation voltage. The cable compensation current at no load is Icab. The cable compensation voltage Vcab can be adjusted by setting the Rvs1 value. Neglecting the forward conduction voltage of the rectifier diode D2, the cable compensation voltage at full load is

$$V_{CAB} = I_{CAB} * R_{VS1} * (N_S/N_A)$$

The output voltage at cable end is approximately

$$V_0 = V_{FB} * (1 + R_{VS1}/R_{VS2}) * (N_S/N_A) - V_D$$

The cable compensation percentage is approximately

VCAB/VO=ICAB /[VFB*(1/Rvs1+1 /Rvs2)]=ICAB*(Rvs1//Rvs2)/VFB-0.05

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The 0.05 is to compensate the load regulation drop. In a typical 5V/1A charger system, ICAB = $50\mu A$, Rvs1= $30K\Omega$, Rvs2= $25K\Omega$ generate about 5% cable compensation voltage at system output.

9.3 Constant Current (CC) operation

Output current is limited by the maximum ratio of secondary winding conduction time (Tons) to the switching period (Tsw). So

$$Iomax=0.5*(Vcsmax/Rcs)*(Np/Ns)*Dsmax$$

Where Dsmax=Tonsmax /Tsw=0.5.

During the constant current operation, if the output voltage is lower than a specified voltage Vsc for 64mS(typical), the output is regarded as shorted to ground, the controller will go into hiccup mode (startup then shutdown repeatedly) until the output voltage is higher than Vsc again.

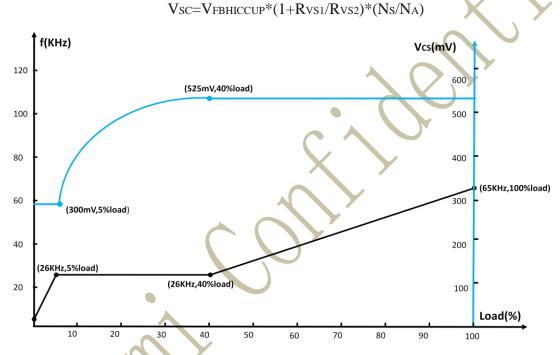


Fig.5, switching frequency and CS voltage vs. load current

9.4 Switching frequency control

The K0107BM-6 operates in Pulse Frequency Modulation (PFM) mode to control output voltage and current. As shown in Fig.6, the CS voltage (Vcs) at the power transistor turnoff instant varies from Vcsmin to Vcsmax when the load increases from no load to full load. Operating frequencies varies from around 100Hz at no load to 65KHz at full load, in the typical 5V/1A USB charger design. The power transistor turns on when the resonant ringing voltage is down to its valley (quasi-resonant switching). This can reduce turn on losses of the power transistor. It can also generate switching period jittering to reduce EMI.

9.5 Built-in output over voltage protection

When the output voltage is over a specified value Vovp for 3 successive switching cycles, the internal output over voltage protection function works, power transistor will be turned off until a new startup event begins.

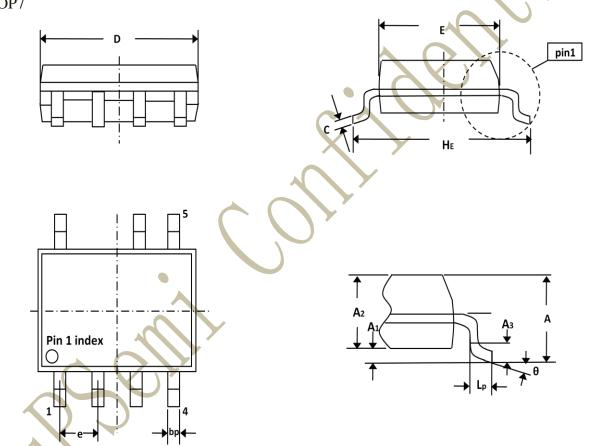
 $V_{OVP}=V_{FBOVP}*(1+R_{VS1}/R_{VS2})*(N_S/N_A)$

10. Ordering information

Part number	Mark ID	Output power	Quantity per real	Package
K0107BM-6	0107BM-6	5W	4,000	SOP-7

11. Mechanical dimensions

SOP7



UNI T	A	A1	A2	A3	bp	С	D	Е	e	HE	Lp	θ
mm	1.75	0.10/ 0.25	1.25/1.5	0.25		0.19/ 0.25	4.7/ 5.1	3.8/ 4.0	1.27	5.8/ 6.2	0.45/ 0.8	8°